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10/689,348

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PTO/SB/97 (09-04)

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PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant:

Stephan Grunow, et al.

Serial No.:

10/689,348

Filed:

October 20, 2003

Title:

LOW K DIELECTRIC INTEGRATED CIRCUIT

INTERCONNECT STRUCTURE

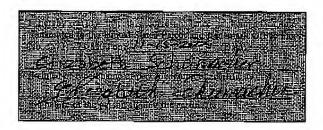
Grp./A.U.:

2829

Examiner:

Dana Farahani

Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450



Sir:

AMENDMENT UNDER 37 C.F.R. § 1.111

In response to the Examiner's Action mailed August 23, 2005, please amend the aboveidentified application as follows: